FIG._1
(PRIOR ART)

FIG._2

APPLY TEST REQUEST SIGNAL
FROM FIRST CIRCUIT BOARD
TO SECOND CIRCUIT BOARD
310

LATCH PRESENT STATE OF
FIRST FUNCTIONAL SIGNAL IN
RESPONSE TO TEST REQUEST
SIGNAL
320

EXCHANGE SIGNALS BETWEEN
FIRST CIRCUIT BOARD AND
SECOND CIRCUIT BOARD TO
TEST SIGNAL PATH FOR FIRST
FUNCTIONAL SIGNAL
330

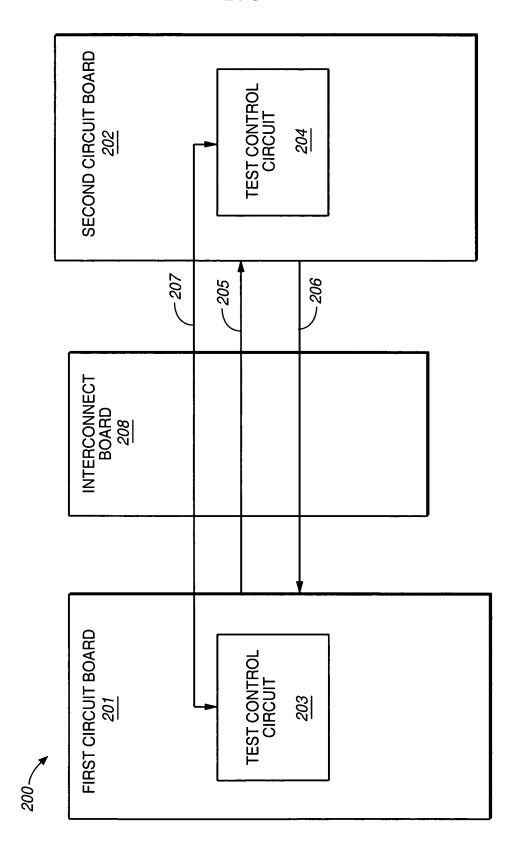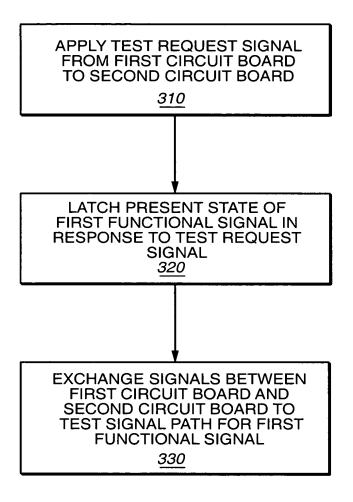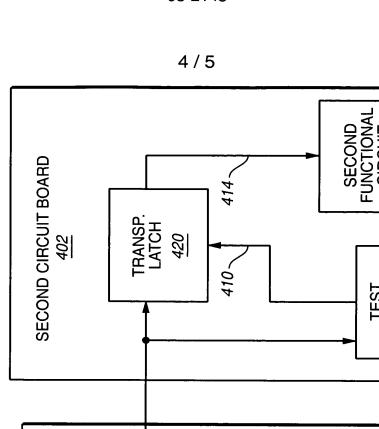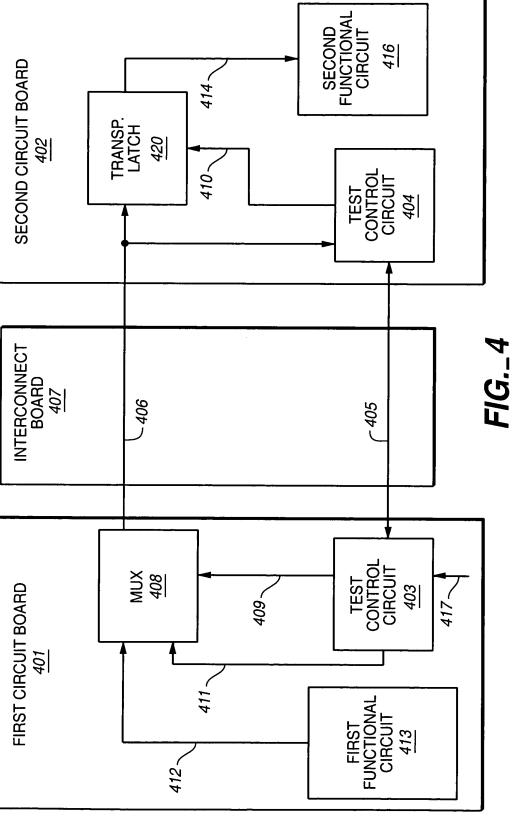*FIG._3*

FIG._4

APPLY TEST REQUEST SIGNAL
510

APPLY CONTROL INPUT
SIGNAL WITHIN CIRCUIT 402
520

LATCH PRESENT STATE OF FUNCTIONAL
SIGNAL PATH 406 AND UTILIZE LATCHED
SIGNAL FOR CONTINUED FUNCTIONING
OF CIRCUIT 402
530

RETURN TEST REQUEST ACCEPTED
INDICATION TO CIRCUIT 401
535

APPLY SELECTION SIGNAL
540

OUTPUT TEST SIGNAL TO LATCH 420
550

DETECT TEST SIGNALS IN TEST
CONTROL CIRCUIT 404
560

RETURN TEST RESULTS TO
CIRCUIT 403
570

APPLY UNLATCH INSTRUCTION
580

FIG._5